

Question 1: The CONSTATE.CLK = Clock is used to indicate that the _____ state variables change on a clock transition.

- CONSTATE
- FLOOR
- MOTION
- OPEN

Question 2: A frequency counter _____

- Counts pulse width
- Counts no. of clock pulses in 1 second
- Counts high and low range of given clock pulse
- None of given options

Question 3: The outputs of SR latches in elevator state machine are feed back to the _____ gate array for connection to the D-flipflops.

- NOT
- AND
- OR
- XOR

Question 4: A 8-bit serial in / parallel out shift register contains the value "8", _____ clock signal(s) will be required to shift the value completely out of the register.

- 1
- 2
- 4
- 8

Question 5: Excess-8 code assigns _____ to "+7"

- 0
- 1001
- 1000
- 1111

Question 6: In _____ the Q output of the lasy flip-flop of the shift register is connected to the data input of the first flip-flop of the shift register.

- Moore machine
- Meally machine
- Johnson counter
- Ring counter